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Technology Center 2600

[# Table 1]

Table 1Length: 7bit

<u>Bit</u>	<u>Description</u>
<u>0000000</u>	<u>The previous RLC PDU was exactly filled with the last segment of an RLC SDU.</u>
<u>1111100</u>	<u>Reserved.</u>
<u>1111101</u>	<u>Reserved.</u>
<u>1111110</u>	<u>AM PDU: The rest of the RLC PDU includes a piggybacked STATUS PDU.</u> <u>UM PDU: Reserved.</u>
<u>1111111</u>	<u>The rest of the RLC PDU is padding.</u>

[# Table 2]

Table 2Length: 15bit

<u>Bit</u>	<u>Description</u>
<u>000000000000000</u>	<u>The previous RLC PDU was exactly filled with the last segment of an RLC SDU.</u>
<u>11111111111011</u>	<u>The last segment of an RLC SDU was one octet short of exactly filling the last RLC PDU.</u>
<u>11111111111100</u>	<u>Reserved.</u>
<u>11111111111101</u>	<u>Reserved.</u>
<u>11111111111110</u>	<u>AM PDU: The rest of the RLC PDU includes a piggybacked STATUS PDU.</u> <u>UM PDU: Reserved.</u>
<u>11111111111111</u>	<u>The rest of the RLC PDU is padding.</u>

B. Clean Specification Changes

Please replace paragraphs starting at page 4, line 4 and ending at page 4, line 5 with the following paragraphs:

Table 1

Length: 7bit

Bit	Description
0000000	The previous RLC PDU was exactly filled with the last segment of an RLC SDU.
1111100	Reserved.
1111101	Reserved.
1111110	AM PDU: The rest of the RLC PDU includes a piggybacked STATUS PDU. UM PDU: Reserved.
1111111	The rest of the RLC PDU is padding.

Table 2

Length: 15bit

Bit	Description
000000000000000	The previous RLC PDU was exactly filled with the last segment of an RLC SDU.
11111111111011	The last segment of an RLC SDU was one octet short of exactly filling the last RLC PDU.
11111111111100	Reserved.
11111111111101	Reserved.
11111111111110	AM PDU: The rest of the RLC PDU includes a piggybacked STATUS PDU. UM PDU: Reserved.
11111111111111	The rest of the RLC PDU is padding.